



डिपार्टमेंट ऑफ इलेक्ट्रॉनिक्स इंजीनियरिंग  
DEPARTMENT OF ELECTRONICS ENGINEERING  
सरदार वल्लभभाई नेशनल इंस्टिट्यूट ऑफ टेक्नोलॉजी, सूरत  
SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT

क्रमांक: DECE/ISRO/ 3024 / 2024-25

दिनांक: 21/01/2025

Recruitment of Project Staff  
Junior Research Fellow  
on Purely Contract Basis

**DST Sponsored Research Project:** Onboard spectral preprocessing for multispectral image compression using FPGA

**List of Short Listed Candidates:**

Sr. No.	Name of candidates	Eligible	Remarks
1.	Bandaru Poorna Chandra	Eligible	--

1. Interview for the shortlisted candidates will be held on **24<sup>th</sup> January, 2025** from 10:30 am in Department of Electronics Engineering (New Building). **Short listed candidates must report by 10:00 am in Department office. Candidate, who can't attend interview physically, can send request for Online Interview process through e-mail id: [add@eced.svnit.ac.in](mailto:add@eced.svnit.ac.in) with valid reasons.**
2. Candidate must bring relevant original certificate, experience letter (if any) and 2 photographs at the time of interview.
3. List of selected candidates will be displayed on the institute website within one week after interview held.
4. **No TA/DA will be paid for attending the Interview.**



*Amal*

Dr. A. D. Darji  
PI, ISRO Project